

# PCIe M.2 Socket 3 Test Adapter

User Manual



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### Introduction

This user's guide documents the PCIe M.2 Socket 3 Host and Device Test Adapters (PCIeM2-TPA3-P and PCIeM2-TPA3-R, respectively). The two test adapter types, shown in Figures 1 and 2, test PCIe M.2 Socket 3 hosts and devices against the PCIe Specification.

The TPA-P and TPA-R test adapter assemblies allow easy access, via SMA connections, to measure or inject data signals. The Receptacle test adapter also provides access to +3.3V, and their respective GNDs, via a 4-position power connector. Additionally, a 14-position low-speed connector is provided for access to commonly used low-speed signals. Mating 4-position power cables and 14-position connector housing and contacts are provided to connect the above power and/or a low speed wiring assembly.

**NOTE: To avoid damaging the cables, use the handling techniques described in the Care and Handling section before making any connections or configuring a test setup.**

**Always use a static-safe workstation when performing tests, as explained in the "Electrostatic Discharge Information" section.**

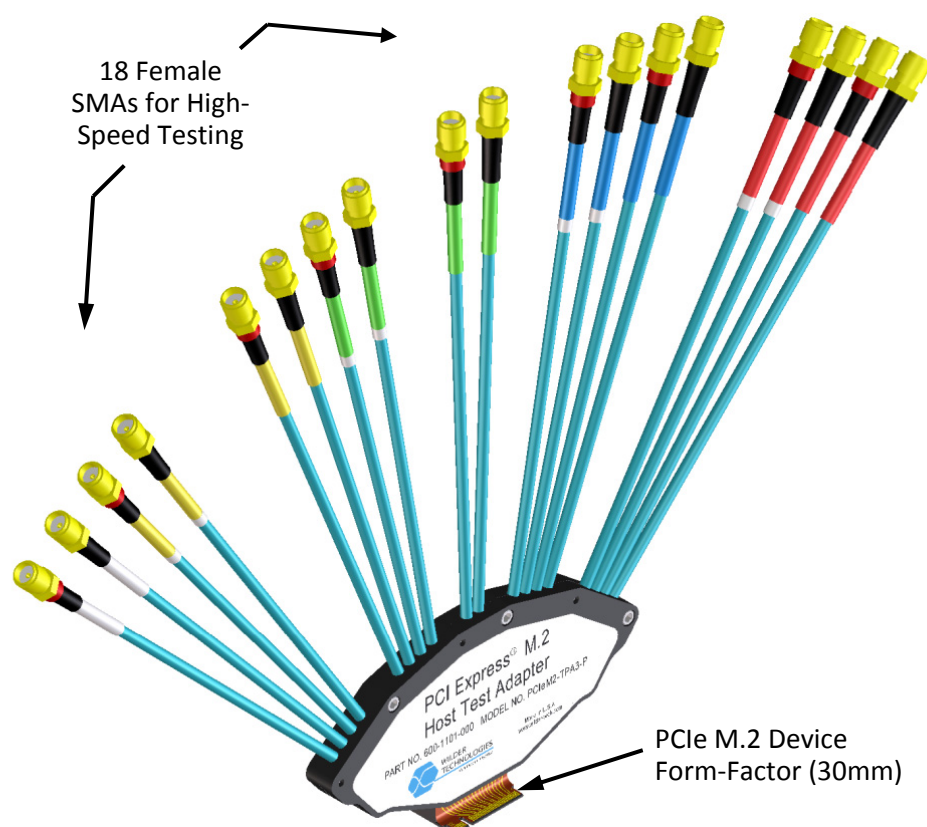
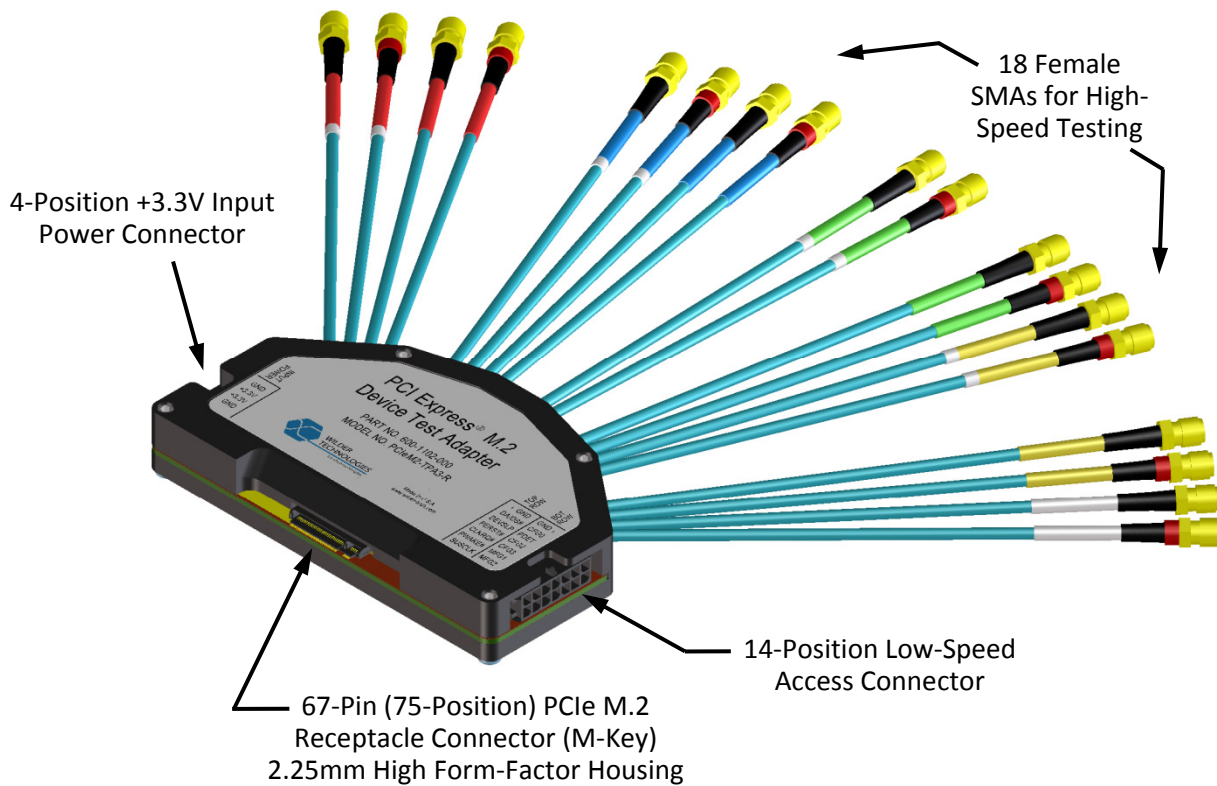


Figure 1. The PCIe M.2 Socket 3 Host Test Adapter (Plug)



**Figure 2. The PCIe M.2 Socket 3 Device Test Adapter (Receptacle)**

The 4-position input power connector used in the receptacle test adapter is Molex part number 43650-0404 and is used with one of the two versions of input power cable assemblies, provided with each PCIeM2-TPA3-R. The 14-position low-speed connector is Molex part number 43045-1402. A mating 14-position receptacle connector housing and contact pins for 26-30awg wire are provided with each PCIeM2-TPA3-R assembly (Molex part number 43025-1400 for the 14-position low-speed connector housing, and 43030-0011 for the 26-30awg pin contacts). Replacement receptacle housings or contacts can be purchased through Molex distributors.

## Product Inspection

Upon receiving the PCIeM2-TPA3 from Wilder Technologies, perform the following product inspection:

- Inspect the outer shipping container, foam-lined instrument case, and product for damage. Retain the outer cardboard shipping container until the contents of the shipment have been inspected for completeness and the product has been checked mechanically and electrically. Use the foam-lined instrument-case for secure storage of the Wilder Technologies PCIe M.2 Socket 3 Test Adapter when not in use.
- Locate the shipping list and verify that all items ordered were received.
- In the unlikely event that the product is defective or incomplete, the “Limited Warranty” section discusses how to contact Wilder Technologies for technical assistance and/or how to package the product for return.

## The PCIe M.2 Socket 3 Test Adapter Care and Handling Precautions

The PCIe M.2 Socket 3 Test Adapter requires careful handling to avoid damage. Improper handling techniques, or using too small a cable bend radius, can damage the coaxial cable connections within the adapter housing or the cables themselves. This can occur at any point along the cable. To achieve optimum performance and to prolong the PCIeM2-TPA3's life, observe the following handling precautions:

- **CAUTION 1: Avoid Torque Forces (Twisting)**  
While individual coaxial cables within the test adapter have some rotational freedom, twisting the PCIeM2-TPA3 as a unit, with one end held stationary, may damage or severely degrade performance. Adherence to Caution 5 (below) helps to avoid twisting.
- **CAUTION 2: Avoid Sharp Cable Bends**  
Never bend coaxial cables into a radius of 26 mm (1-inch) or less. Never bend cables greater than 90°. Single or multiple cable bends must be kept within this limit. Bending the PCIeM2-TPA3 cables less than a 26mm (1-Inch) radius will permanently damage or severely degrade test adapter performance.
- **CAUTION 3: Avoid Cable Tension (Pull Forces)**  
Never apply tension (pull forces) to an individual coaxial cable that is greater than 2.3 kg (5 lbs.). To avoid applying tension, always place accessories and equipment on a surface that allows adjustment to eliminate tension on the PCIeM2-TPA3 and cables. Use adjustable elevation stands or apparatus to accurately place and support the PCIeM2-TPA3.
- **CAUTION 4: Connect the PCIeM2-TPA3 First**  
To prevent twisting, bending, or applying tension to the coaxial cables when connecting a PCIeM2-TPA3, always attach the PCIeM2-TPA3 to the device under test (DUT) or cable under test before attaching any SMA connectors. Carefully align the PCIe M.2 connectors and then gently push the connectors together until fully seated (see also Caution 7).

If the PCIeM2-TPA3 must be turned or twisted to make connection to the DUT, avoid using the PCIeM2-TPA3 housing alone to make this occur. Try to distribute the torque forces along the length of the test setup and cabling. If this is not possible, it is recommended to first loosen or disconnect the SMA connections at the PCIeM2-TPA3, make the connection to the DUT and then re-tighten or attach the test equipment leads.

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**NOTE: Only grip the test adapter housing when inserting or extracting the PCIeM2-TPA3 to or from the DUT. Pulling directly on the PCIeM2-TPA3 cables or using them to insert the PCIeM2-TPA3 may cause damage.**

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- **CAUTION 5: Carefully Make SMA Connections**

To connect the PCIeM2-TPA3 SMA connectors, follow these steps:

1. Hold the cable stationary by grasping the cable at the black heat-shrink section near the SMA connector.
2. Insert the mating SMA barrel and hand-tighten the free-spinning SMA nut onto the connector while avoiding pulling, bending, or twisting the PCIeM2-TPA3 coaxial cable.
3. The PCIeM2-TPA3 SMA connectors have flats that accept an open-end 1/4-inch or 6.5mm wrench. When attaching instrument cables to the PCIeM2-TPA3, it is recommended that the PCIeM2-TPA3 SMA connectors be mechanically held and the test leads be tightened to the equipment manufacturer's torque recommendations, normally 5 in-lbs, using a 5/16-inch open-end wrench.

If the test set-up requires repositioning, first loosen or disconnect the SMA connections to avoid twisting, bending, or tension.

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**NOTE: A drop in signal amplitude by half or 6db during the testing of a channel may indicate that a cable has been mechanically pulled free of coaxial cable connections internal to the assembly. This could be determined by checking if the cable has any lateral play relative to the TPA. This would only occur when the TPA has exceeded the pull force as specified within the mechanical specification. If the cable cannot be re-seated, the test adapter will need to be sent back to the factory for service.**

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- **CAUTION 6: Independently Support Instrument Cables or Accessories**

Excessive weight from instrument cables and/or accessories connected to the PCIeM2-TPA3 can cause damage or affect the test adapter performance. Be sure to provide appropriate means to support and stabilize all test set-up components.

- **CAUTION 7: Observe M.2 (NGFF) Mini-Card Module Insertion/Removal Instructions**

Users that are unfamiliar with the M.2 (NGFF) connector interface and the associated module (device or TPA) insertion and removal process are encouraged to review Tyco Electronics document 114-115006, Section 3.13 for guidance (see link, below and the extracted guideline section on the following page). *(Courtesy of TE Connectivity)*

[http://www.te.com/commerce/DocumentDelivery/DDEController?Action=showdoc&DocId=Specification+Or+Standard%7F114-115006%7FA%7Fpdf%7FEnglish%7FENG\\_SS\\_114-115006\\_A.pdf%7FN-A](http://www.te.com/commerce/DocumentDelivery/DDEController?Action=showdoc&DocId=Specification+Or+Standard%7F114-115006%7FA%7Fpdf%7FEnglish%7FENG_SS_114-115006_A.pdf%7FN-A)

- **CAUTION 7 (Continued): M.2 (NGFF) Mini-Card Module Insertion/Removal Instructions**  
Extracted from Tyco Electronics Application Note 114-115006 (Courtesy of TE Connectivity).

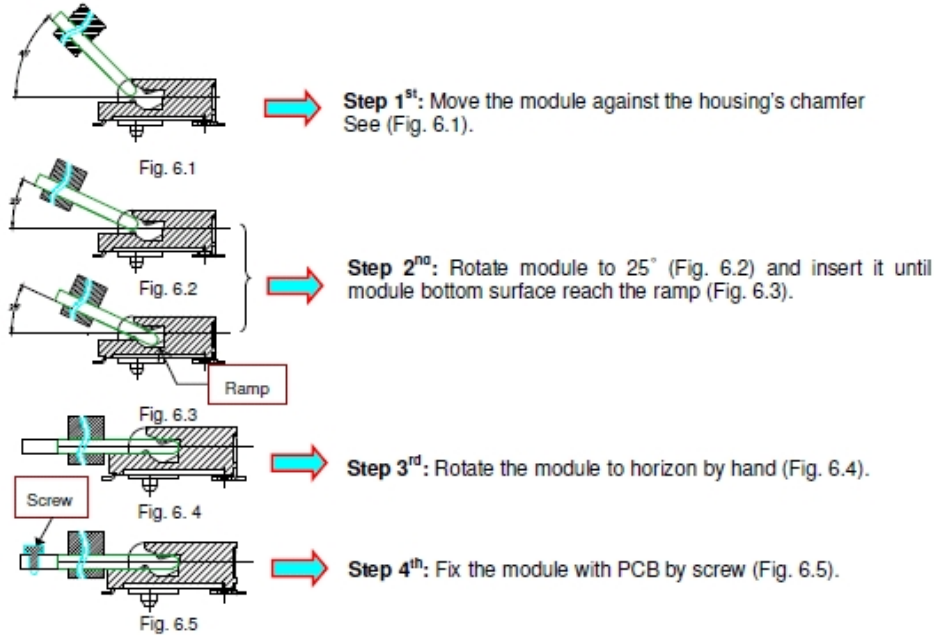


67 Positions 0.5 Pitch M.2(NGFF) MINICARD

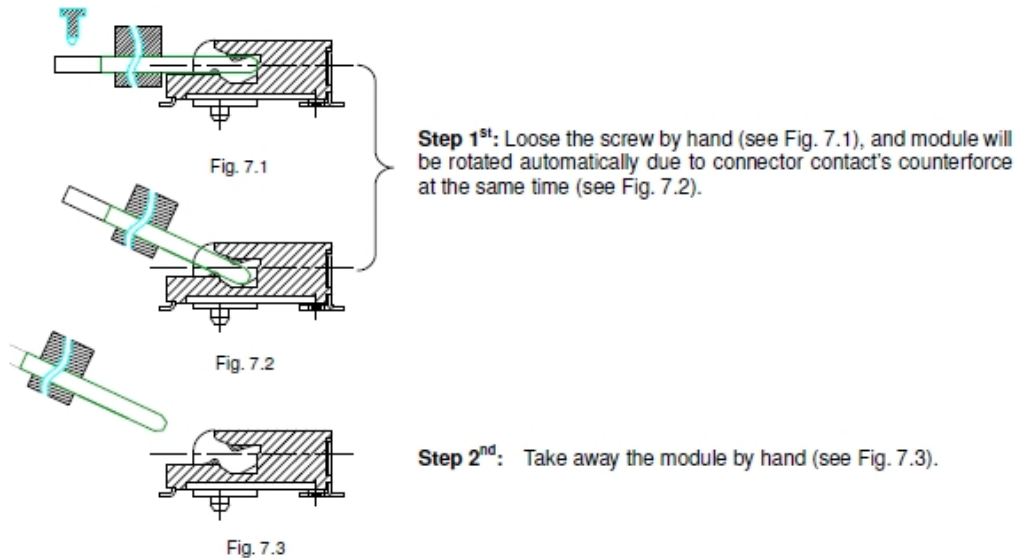
114-115006

### 3.13. Module Insertion and Removal Process

#### A. Module Insertion Process for 2.25H, 3.2H, and 4.2H



#### B. Module Removal Process for 2.25H, 3.2H, and 4.2H



## General Test Adapter, Cable, and Connector

Observing simple precautions can ensure accurate and reliable measurements.

### Handling and Storage

Before each use of the PCIeM2-TPA3, ensure that all connectors are clean. Handle all cables carefully and store the PCIeM2-TPA3 in the foam-lined instrument case when not in use, if possible. Do not set connectors contact end down. Install the SMA protective end caps when the PCIeM2-TPA3 is not in use.

### Visual Inspection

Be sure to inspect all cables carefully before making a connection. Inspect all cables for metal particles, scratches, deformed threads, dents, or bent, broken, or misaligned center conductors. Do not use damaged cables.

### Cleaning

If necessary, clean the connectors using low-pressure (less than 60 PSI) compressed air or nitrogen with an effective oil-vapor filter and condensation trap. Clean the cable threads, if necessary, using a lint-free swab or cleaning cloth moistened with isopropyl alcohol. Always completely dry a connector before use. Do not use abrasives to clean the connectors. Re-inspect connectors, making sure no particles or residue remains.

### Making Connections

Before making any connections, review the “Care and Handling Precautions” section. Follow these guidelines when making connections:

- Align cables carefully
- Make preliminary connection lightly
- To tighten, turn connector nut only
- Do not apply bending force to cable
- Do not over-tighten preliminary connections
- Do not twist or screw-in cables
- Use an appropriately sized torque wrench (depends on SMA gender), and do not tighten past the “break” point of the torque wrench (normally set to 5 in-lbs)

## Electrostatic Discharge Information

Protection against electrostatic discharge (ESD) is essential while connecting, inspecting, or cleaning the PCIeM2-TPA3 test adapter and connectors attached to a static-sensitive circuit (such as those found in test sets).

Electrostatic discharge can damage or destroy electronic components. Be sure to perform all work on electronic assemblies at a static-safe work station, using two types of ESD protection:

- Conductive table-mat and wrist-strap combination
- Conductive floor-mat and heel-strap combination

When used together, both of these types provide a significant level of ESD protection. Used alone, the table-mat and wrist-strap combination provide adequate ESD protection. To ensure user safety, the static-safe accessories must provide at least 1 MΩ of isolation from ground. Acceptable ESD accessories may be purchased from a local supplier.

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**WARNING: These techniques for a static-safe work station should not be used when working on circuitry with a voltage potential greater than 500 volts.**

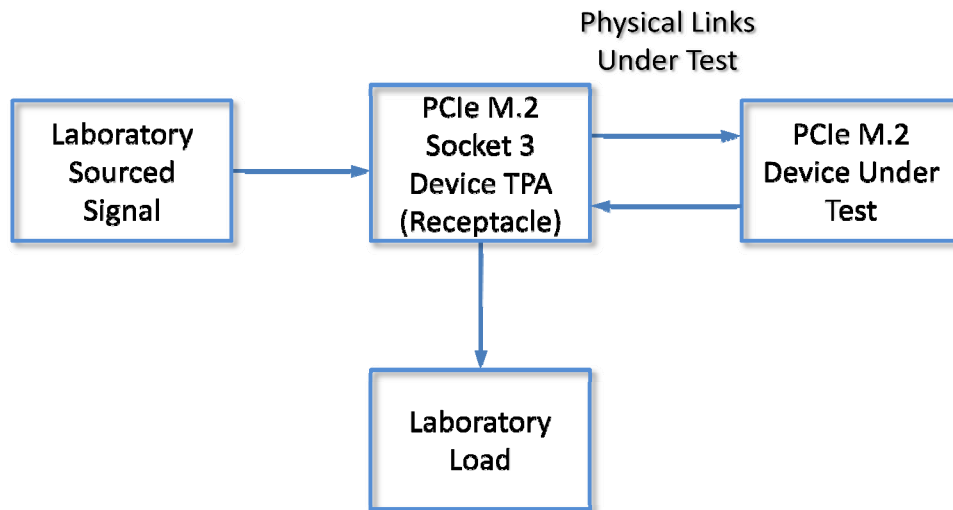
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## User Model

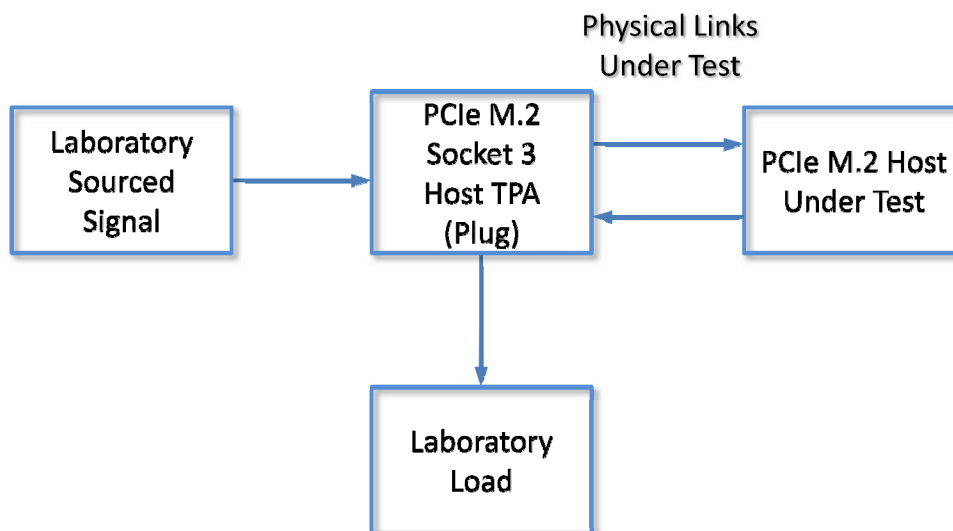
The PCIeM2-TPA3 supports all testing of the PCIe Specification. It is capable of performing well beyond the scope of measurements required, limited only by the specifications, environmental, care and handling as stated in this document.

The following examples are suggestions for possible testing setups.

In this first example, a PCIe M.2 Socket 3 Device (Receptacle) TPA is used to test a PCIe M.2 Socket 3 device:



The second example shows a PCIe M.2 Socket 3 Host (Plug) TPA used to test a host:



## Calibration Through De-Embedding

The PCIe M.2 Socket 3 Host and Device Test Adapters are fully passive components. Therefore, calibration compensating for the losses must occur within the test instrumentation that drives the PCIe M.2 receiver device or looks at the response of the PCIe M.2 transmitter device.

The PCIeM2-TPA3's have Touchstone S4P files for de-embedding the electrical length and losses within the TPA up to the PCIe M.2 connector interface pads. (Contact Wilder Technologies, [support@wilder-tech.com](mailto:support@wilder-tech.com), to obtain a copy of the S4P files.) The Touchstone S4P files enable the test engineer to compensate for the following six repeatable, systematic errors that occur when moving the reference plane:

- Signal leakage effects: *Directivity errors*
- Signal leakage effects: *Crosstalk errors*
- Reflection effects: *Source Impedance Mismatching errors*
- Reflection effects: *Load Impedance Mismatching errors*
- Bandwidth effects: *Receiver Transmission in Test Equipment errors*
- Bandwidth effects: *Receiver Reflection-tracking in Test Equipment errors*

These errors are corrected on each port. Refer to the Instrument Manual for instructions on the instrument's specific de-embedding process.

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**NOTE: The reference plane is the boundary, both physically and electrically, between the calibrated and uncalibrated portions of the circuit. Everything outside the reference plane is considered part of the DUT. Any instrument that does not use calibration or de-embedding of the test fixture defines the DUT as the total of externally connected components. If the de-embedding file is not used, all of the PCIeM2-TPA3 and associated coaxial cables, as well as cables connecting the TPA assembly to the test instrument, would be a part of the DUT.**

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Non-repeatable errors, such as drift or random errors, can be reduced but not corrected. Drift errors aggregate over time or with environmental changes such as temperature shift. To eliminate drift errors, perform an instrumentation-level calibration.

A random error cannot be corrected through calibration since the error occurred randomly. Random errors are typically associated with either test instrument noise or test repeatability problems. Reduce test instrument noise by increasing source power, lowering the IF bandwidth, or averaging results over multiple sweeps. Reduce test repeatability problems through the use of a torque wrench or, again, by averaging over multiple sweeps.

## Mechanical and Environmental Specifications

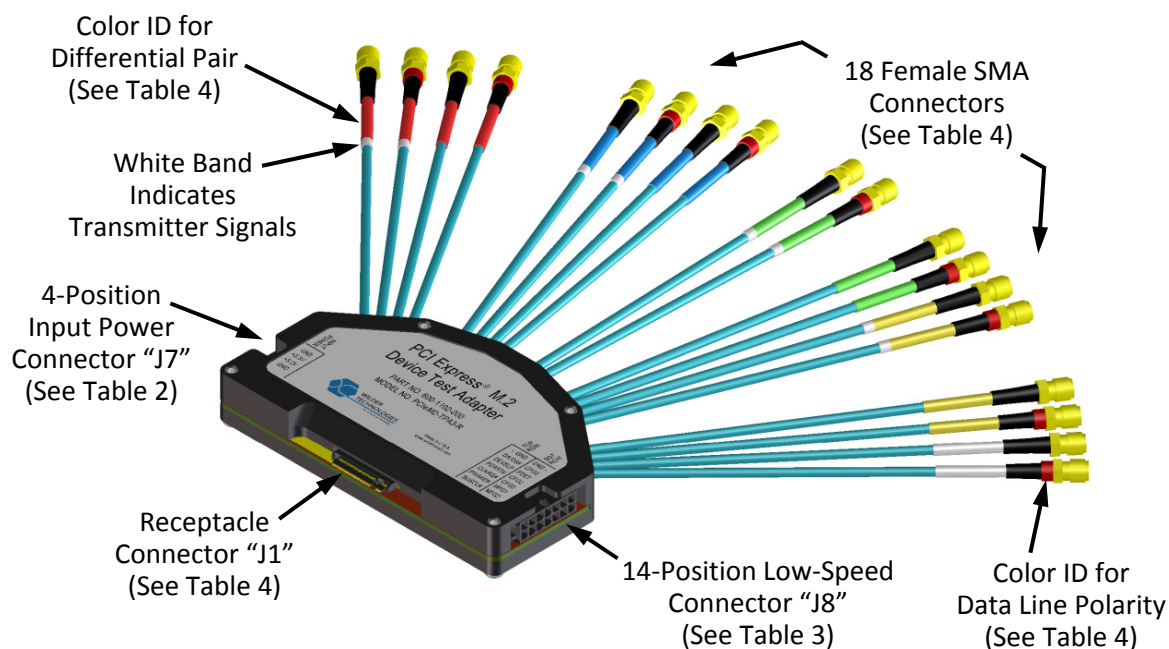
**NOTE: All specifications in this manual are subject to change.**

**Table 1. General Specifications**

ITEM	DESCRIPTION
Usage Environment	Controlled indoor environment
Host Test Adapter Length (w/std. cables)	187mm +/- 2mm (7.36 inches +/- .08 inches) (Characteristic)
Device Test Adapter Length (w/std. cables)	199mm +/- 2mm (7.84 inches +/- .08 inches) (Characteristic)
Operating Temperature	0°C to +55°C (32°F to +131°F) (Characteristic)
Storage Temperature	-40°C to +70°C (-40°F to +158°F) (Characteristic)

### PCIe M.2 Socket 3 Device TPA (PCIeM2-TPA3-R) Cable Pinout

The PCIeM2-TPA3-R cables provide 18 SMA connectors (four lanes of primary differential receive and transmit, and reference clock), one 4-position input power connector, and one 14-position connector containing the low-speed signals. Labels clearly mark each cable or connector. The following figure refers to pin-description tables for each of the three connector types. Note: The input power supply (3.3V) has a 22 $\mu$ F in parallel with two-each 0.01 $\mu$ F capacitors.



**Figure 3. Cable Connectors (PCIeM2-TPA3-R shown)**

**Table 2. PCIe M.2 Socket 3 Device TPA 4-Position Input Power Connector “J7”**

<b>LABEL</b>	<b>PIN NO.</b>	<b>DESCRIPTION</b>
GND	Pin 1	Ground
+3.3V	Pin 2	+3.3 Volts
+3.3V	Pin 3	+3.3 Volts
GND	Pin 4	Ground












**Table 3. PCIe M.2 Socket 3 Device TPA 14-Position Low-Speed Connector “J8”**

<b>LABEL</b>	<b>PIN NO.</b>	<b>DESCRIPTION</b>
GND	Pin 1	Ground
CFG0	Pin 2	Configuration 0
PDET	Pin 3	Interface Detect
CFG2	Pin 4	Configuration 2
CFG3	Pin 5	Configuration 3
MFG1	Pin 6	Vendor Specific / Manufacturing Pin
MFG2	Pin 7	Vendor Specific / Manufacturing Pin
GND	Pin 8	Ground
DA/DS#	Pin 9	Drive Activity Signal (active-high), Disable Staggered Spin-Up (active-low)
DEVSLP	Pin 10	Device Sleep
PERST#	Pin 11	PE-Reset (Functional Reset) (active-low)
CLKRQ#	Pin 12	Clock Request (active-low)
PWAKE#	Pin 13	PCIe PME Wake (active-low)
SUSCLK	Pin 14	Suspend Clock is a 32.768 kHz clock input that is provided by the user as needed

**Table 4. PCIe M.2 Socket 3 Device TPA (Receptacle) Pin Assignments**

Pin Description	Connector Pin Number	Destination Number	Color ID for Data Line Polarity	Color ID for Differential Pair (Receptacle)
Configuration 3	1	CFG3	N/A	N/A
+3.3 Volts	2, 4, 12, 14, 16, 18, 70, 72, 74	+3.3V	N/A	N/A
Ground	3, 9, 15, 27, 33, 39, 45, 51, 57, 71, 73	GND	N/A	N/A
PETn3 (Device Test Transmitter 3 Negative)	5	DEV PET3-	Black	Red/White Band
No connections	6, 8, 20, 22, 24, 26, 28, 30, 32, 34, 36, 40, 42, 44, 46, 48, 67	No Connect	N/A	N/A
PETp3 (Device Test Transmitter 3 Positive)	7	DEV PET3+	Red	Red/White Band
Drive Activity Signal / Disable Staggered Spin-Up	10	DA/DS#	N/A	N/A
PERn3 (Device Test Receiver 3 Negative)	11	DEV PER3-	Black	Red
PERp3 (Device Test Receiver 3 Positive)	13	DEV PER3+	Red	Red
PETn2 (Device Test Transmitter 2 Negative)	17	DEV PET2-	Black	Blue/White Band
PETp2 (Device Test Transmitter 2 Positive)	19	DEV PET2+	Red	Blue/White Band
Configuration 0	21	CFG0	N/A	N/A
PERn2 (Device Test Receiver 2 Negative)	23	DEV PER2-	Black	Blue
PERp2 (Device Test Receiver 2 Positive)	25	DEV PER2+	Red	Blue
PETn1 (Device Test Transmitter 1 Negative)	29	DEV PET1-	Black	Green/Wht Band
PETp1 (Device Test Transmitter 1 Positive)	31	DEV PET1+	Red	Green/Wht Band
PERn1 (Device Test Receiver 1 Negative)	35	DEV PER1-	Black	Green
PERp1 (Device Test Receiver 1 Positive)	37	DEV PER1+	Red	Green
Device Sleep	38	DEVSLP	N/A	N/A
PETn0 (Device Test Transmitter 0 Negative)	41	DEV PET0-	Black	Yellow/Wht Band
PETp0 (Device Test Transmitter 0 Positive)	43	DEV PET0+	Red	Yellow/Wht Band
PERn0 (Device Test Receiver 0 Negative)	47	DEV PER0-	Black	Yellow
PERp0 (Device Test Receiver 0 Positive)	49	DEV PER0+	Red	Yellow

Table 4. PCIe M.2 Socket 3 Device TPA (Receptacle) Pin Assignments (Continued)

Pin Description	Connector Pin Number	Destination Number	Color ID for Data Line Polarity	Color ID for Differential Pair (Receptacle)	
PERST# (PE-Reset)	50	PERST#	N/A	N/A	
CLKREQ# (Clock Request)	52	CLKRQ#	N/A	N/A	
REFCLKN	53	DEV RCLK-	Black	White	
PEWAKE# (PCIe PME Wake)	54	PWAKE#	N/A	N/A	
REFCLKP	55	DEV RCLK+	Red	White	
MFG-1 (Vendor Specific / Manufacturing Pin)	56	MFG1	N/A	N/A	
MFG-2 (Vendor Specific / Manufacturing Pin)	58	MFG2	N/A	N/A	
Key-M	Displaced pins: 59, 60, 61, 62, 63, 64, 65, 66	N/A	N/A	N/A	
SUSCLK (Suspend Clock)	68	SUSCLK	N/A	N/A	
PEDET (Interface Detect)	69	PDET	N/A	N/A	
Configuration 2	75	CFG2	N/A	N/A	

### PCIeM2-TPA3-P Cable Pinout

The PCIeM2-TPA3-P cables provide 18 SMA connectors (four lanes of primary differential, and reference clock). Labels clearly mark each cable or connector. The following figure refers to the pin-description table for the plug connector.

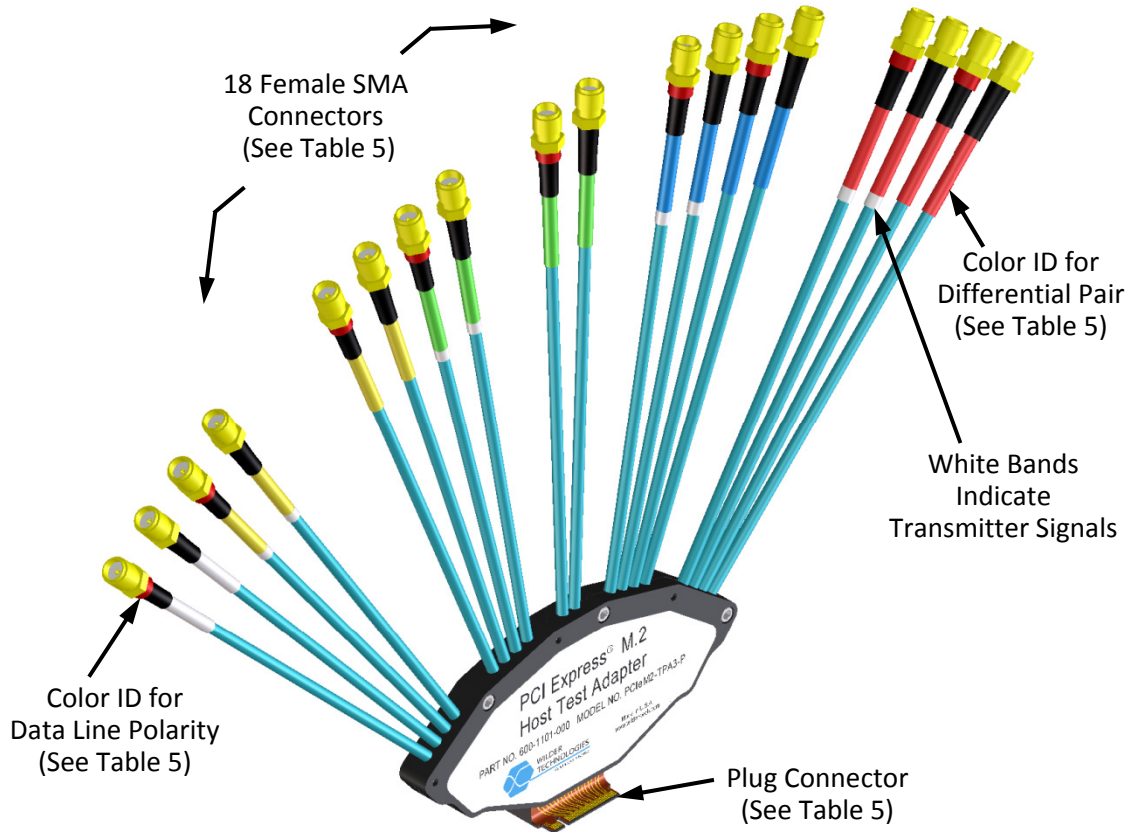


Figure 4. Cable Connectors (PCIeM2-TPA3-P shown)

**Table 5 PCIe M.2 Socket 3 Host TPA (Plug) Pin Assignments**

Pin Description	Connector Pin Number	Destination Number	Color ID for Data Line Polarity	Color ID for Differential Pair (Receptacle)
Configuration 3, Connected to Ground	1	CFG3	N/A	N/A
3.3V (+3.3 Volts)	2, 4, 12, 14, 16, 18, 70, 72, 74	No Connect	N/A	N/A
Ground	3, 9, 15, 27, 33, 39, 45, 51, 57, 71, 73	GND	N/A	N/A
PERn3 (Device Test Receiver 3 Negative)	5	HOST PER3-	Black	Red
No connect (used for other purposes)	6, 8, 20, 22, 24, 26, 28, 30, 32, 34, 36, 40, 42, 44, 46, 48, 67	No Connect	N/A	N/A
PERp3 (Device Test Receiver 3 Positive)	7	HOST PER3+	Red	Red
DAS/DSS# (Drive Activity Signal / Disable Staggered Spin-Up)	10	No Connect	N/A	N/A
PETn3 (Device Test Transmitter 3 Negative)	11	HOST PET3-	Black	Red/White Band
PETp3 (Device Test Transmitter 3 Positive)	13	HOST PET3+	Red	Red/White Band
PERn2 (Device Test Receiver 2 Negative)	17	HOST PER2-	Black	Blue
PERp2 (Device Test Receiver 2 Positive)	19	HOST PER2+	Red	Blue
Configuration 0, Connected to Ground	21	CFG0	N/A	N/A
PETn2 (Device Test Transmitter 2 Negative)	23	HOST PET2-	Black	Blue/White Band
PETp2 (Device Test Transmitter 2 Positive)	25	HOST PET2+	Red	Blue/White Band
PERn1 (Device Test Receiver 1 Negative)	29	HOST PER1-	Black	Green
PERp1 (Device Test Receiver 1 Positive)	31	HOST PER1+	Red	Green
PETn1 (Device Test Transmitter 1 Negative)	35	HOST PET1-	Black	Green/Wht Band
PETp1 (Device Test Transmitter 1 Positive)	37	HOST PET1+	Red	Green/Wht Band
DEVSLP (Device Sleep)	38	No Connect	N/A	N/A
PERn0 (Device Test Receiver 0 Negative)	41	HOST PER0-	Black	Yellow
PERp0 (Device Test Receiver 0 Positive)	43	HOST PER0+	Red	Yellow
PETn0 (Device Test Transmitter 0 Negative)	47	HOST PET0-	Black	Yellow/Wht Band
PETp0 (Device Test Transmitter 0 Positive)	49	HOST PET0+	Red	Yellow/Wht Band



Table 5 PCIe M.2 Socket 3 Host TPA (Plug) Pin Assignments (Continued)

Pin Description	Connector Pin Number	Destination Number	Color ID for Data Line Polarity	Color ID for Differential Pair (Receptacle)
PERST# (PE-Reset)	50	No Connect	N/A	N/A
CLKREQ# (Clock Request)	52	No Connect	N/A	N/A
REFCLKN	53	HOST RCLK-	Black	White
PEWAKE# (PCIe PME Wake)	54	No Connect	N/A	N/A
REFCLKP	55	HOST RCLK+	Red	White
MFG-1 (Vendor Specific / Manufacturing Pin)	56	No Connect	N/A	N/A
MFG-2 (Vendor Specific / Manufacturing Pin)	58	No Connect	N/A	N/A
Key-M	Displaced pins: 59, 60, 61, 62, 63, 64, 65, 66	N/A	N/A	N/A
SUSCLK (Suspend Clock)	68	No Connect	N/A	N/A
PEDET (Interface Detect)	69	No Connect	N/A	N/A
Configuration 2, Connected to Ground	75	CFG2	N/A	N/A



## Electrical Specifications

**NOTE: All specifications in this manual are subject to change.**

**Table 6. Electrical Specifications**

SPECIFICATION	MINIMUM	TYPICAL	MAXIMUM	NOTES
Differential Impedance (ohms), 100 ps Rise Time	95		105	All Differential Pairs, Device TPA and Host TPA, excluding PCIe M.2 connector.
Differential Impedance (ohms), 100 ps Rise Time	85		115	All Differential Pairs, Device TPA and Host TPA, including PCIe M.2 connector.
Impedance (ohms), 100 ps Rise Time	47.5		52.5	All Single-Ended High-Speed Signals, Device TPA and Host TPA, excluding PCIe M.2 connector.
Impedance (ohms), 30 ps Rise Time	46.5		53.5	SMA Connectors
Intra-pair Skew (ps)	-6		6	All Differential Pairs, Device TPA and Host TPA
Inter-pair Skew (ps)	-6		6	All Differential Pairs, Device TPA and Host TPA
NEXT (db), at 8 GHz	-30			All Differential Pairs, single aggressor, without PCIe M.2 connector, with six-inch cables and terminations
Current Carrying (A)	2.5			+3.3V Power

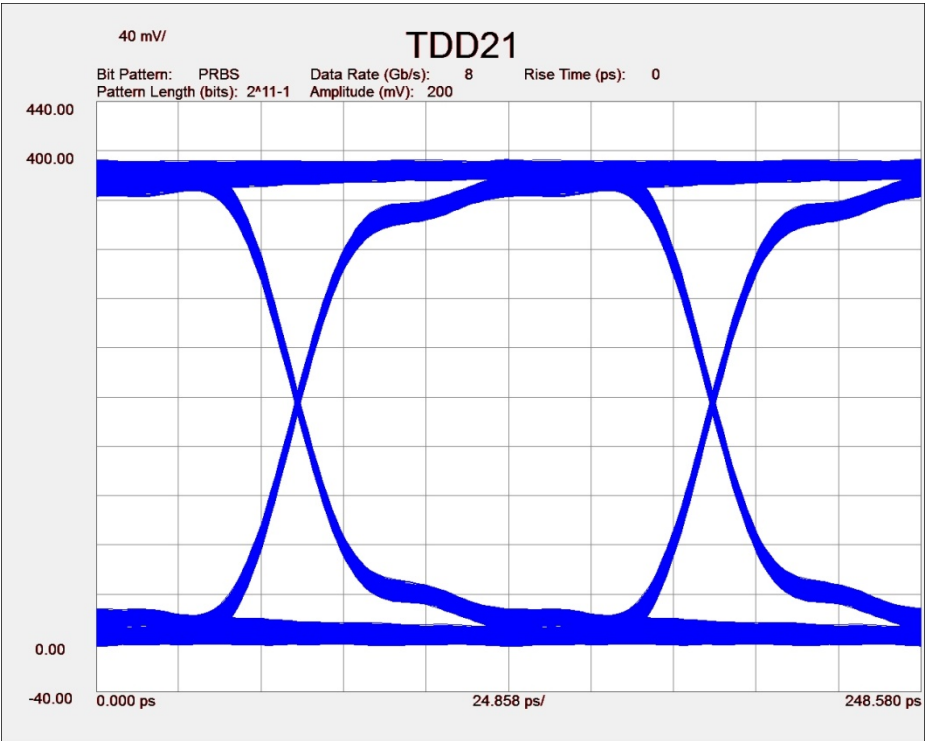
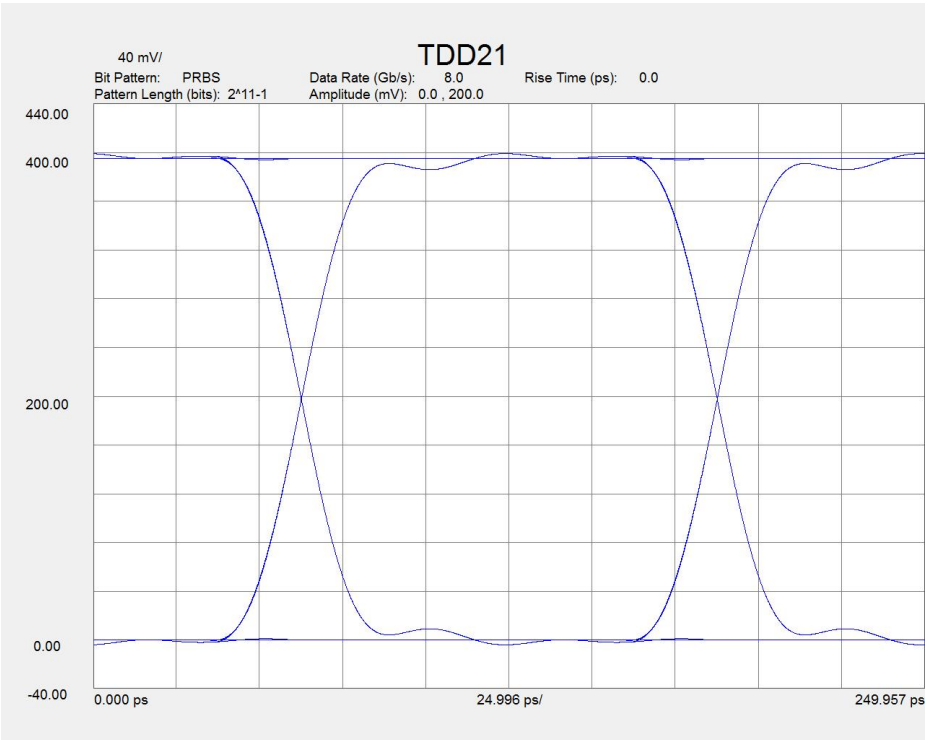


Figure 5. Typical mated pair 8 Gb/s eye diagram, with de-embedding (top) and without de-embedding (bottom)

Parameter Measurement Result	
Name	Result
Eye Level Zero (mV):	-0.8899
Eye Level One (mV):	396.0819
Eye Level Mean (mV):	197.5960
Eye Amplitude (mV):	396.9718
Eye Height (mV):	387.5951
Eye Height (dB):	-0.2736
Eye Width:	125.0000
Eye Opening Factor:	0.9764
Eye Signal to Noise:	127.0086
Eye Duty Cycle Dist:	0.0000
Eye Duty Cycle Dist (%):	0.0000
Eye Rise Time (10-90):	28.3768
Eye Fall Time (10-90):	28.3747
Eye Jitter (PP):	0.0000
Eye Jitter (RMS):	0.0000

Parameter Measurement Result	
Name	Measurement Result
Eye Level Zero(mV)	15.20
Eye Level One(mV)	376.49
Eye Level Mean(mV)	195.85
Eye Amplitude(mV)	361.29
Eye Height(mV)	312.52
Eye Height(db)	-5.05
Eye Width	1.23e-010
Eye Opening Factor	0.87
Eye Signal_to_Noise	17.66
Eye Duty Cycle Dist	-3.26e-014
Eye Duty Cycle Dist(%)	-0.03
Eye Rise Time (20-80)	2.31e-011
Eye Fall Time (80-20)	2.30e-011
Eye Jitter(PP)	2.49e-012
Eye Jitter(RMS)	5.28e-013

Figure 6. Typical mated pair 8 Gb/s eye data, with de-embedding (top) and without de-embedding (bottom)

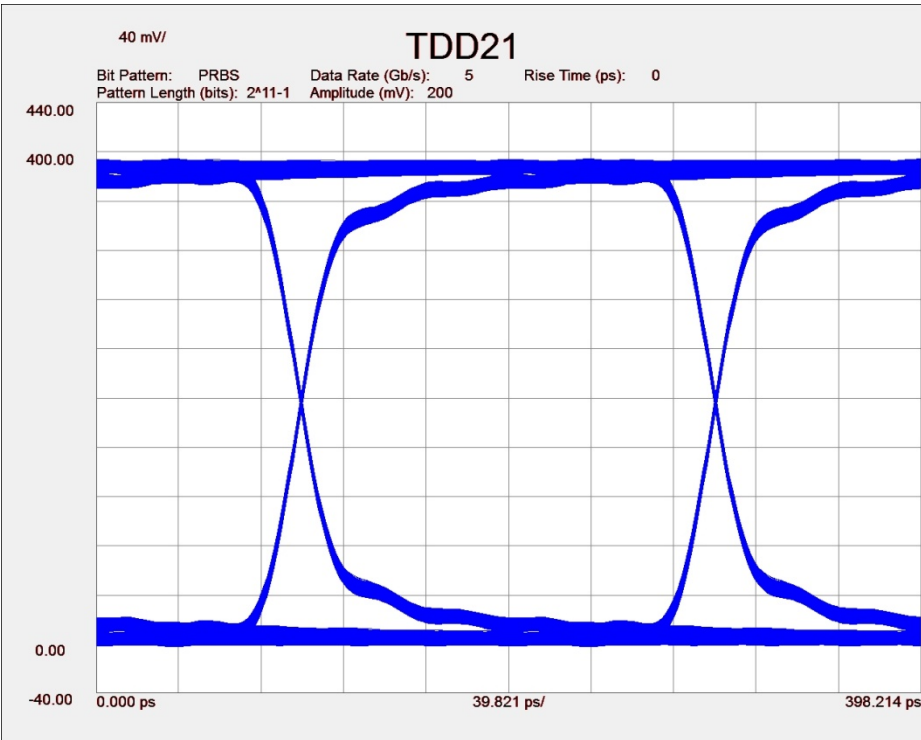
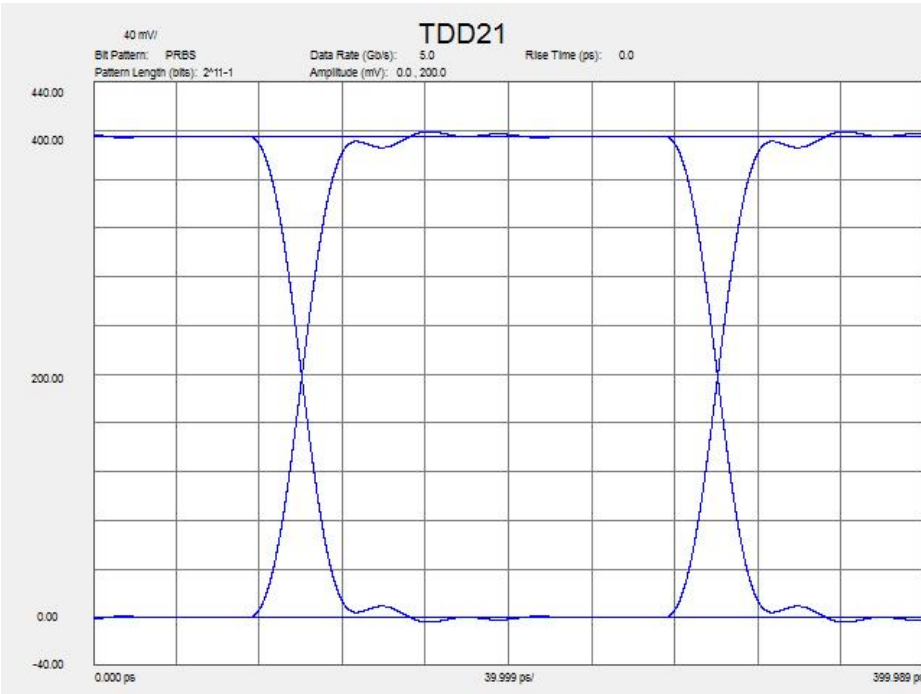


Figure 7. Typical mated pair 5 Gb/s eye diagram, with de-embedding (top) and without de-embedding (bottom)

Parameter Measurement Result	
Name	Result
Eye Level Zero (mV):	-0.1451
Eye Level One (mV):	395.3491
Eye Level Mean (mV):	197.6020
Eye Amplitude (mV):	395.4942
Eye Height (mV):	390.9727
Eye Height (dB):	-0.1983
Eye Width:	197.5085
Eye Opening Factor:	0.9886
Eye Signal to Noise:	262.4079
Eye Duty Cycle Dist:	0.0000
Eye Duty Cycle Dist (%):	0.0000
Eye Rise Time (10-90):	28.1429
Eye Fall Time (10-90):	28.1419
Eye Jitter (PP):	0.2940
Eye Jitter (RMS):	0.4047

Parameter Measurement Result	
Name	Measurement Result
Eye Level Zero(mV)	10.89
Eye Level One(mV)	381.58
Eye Level Mean(mV)	196.24
Eye Amplitude(mV)	370.68
Eye Height(mV)	337.94
Eye Height(db)	-4.71
Eye Width	1.98e-010
Eye Opening Factor	0.91
Eye Signal_to_Noise	25.00
Eye Duty Cycle Dist	-3.44e-015
Eye Duty Cycle Dist(%)	-0.00
Eye Rise Time (20-80)	2.38e-011
Eye Fall Time (80-20)	2.38e-011
Eye Jitter(PP)	2.22e-012
Eye Jitter(RMS)	4.16e-013

Figure 8. Typical mated pair 12 Gb/s eye data, with de-embedding (top) and without de-embedding (bottom)

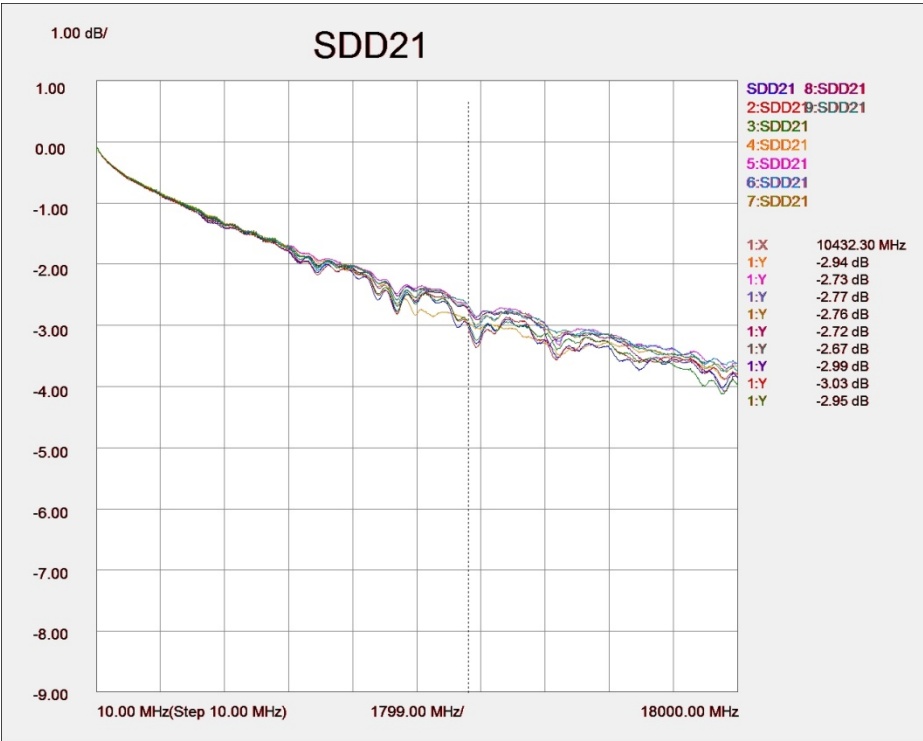
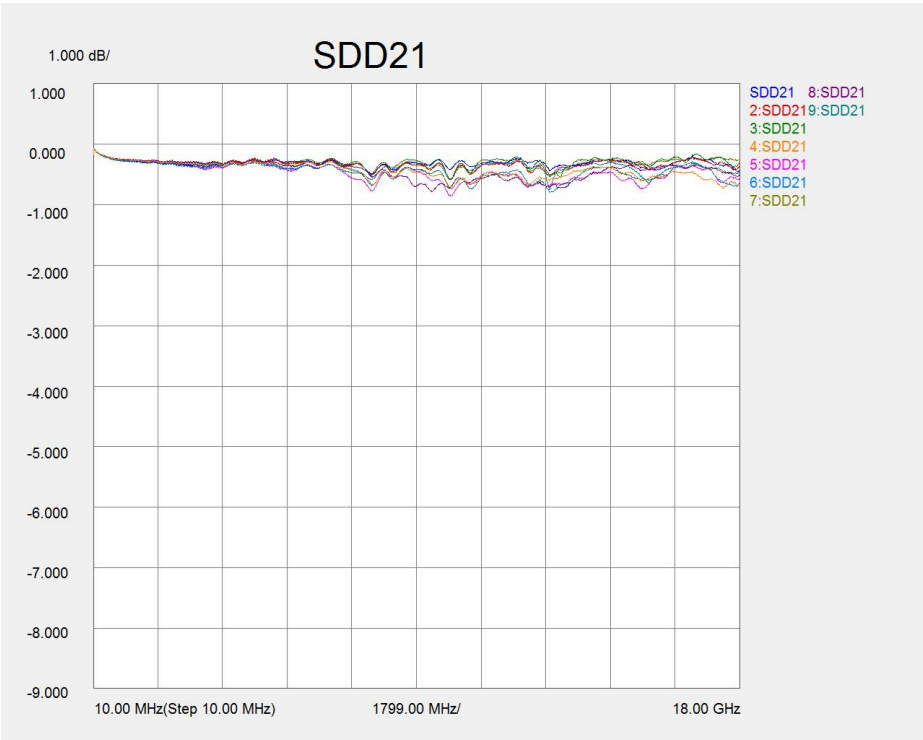


Figure 9. Typical mated pair balanced insertion loss, with de-embedding (top) and without de-embedding (bottom)

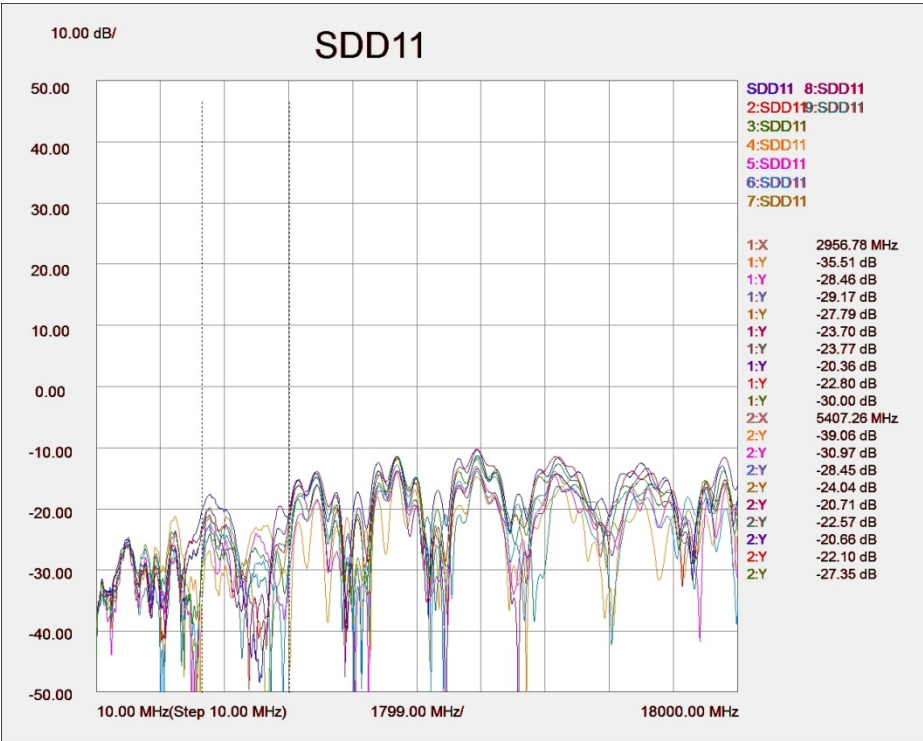
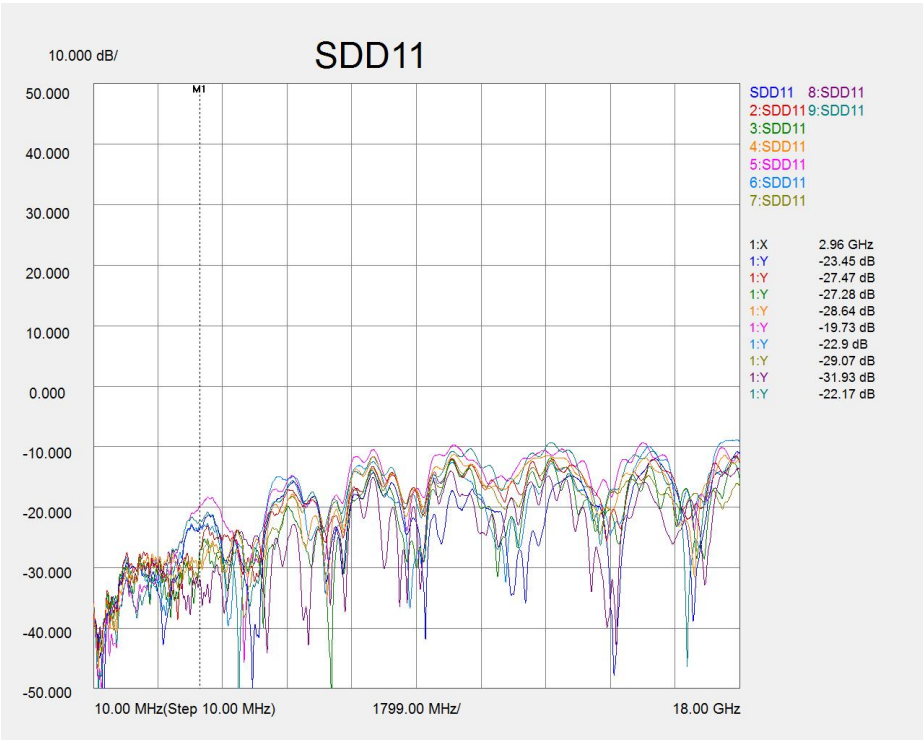


Figure 10. Typical mated pair balanced return loss, with de-embedding (top) and without de-embedding (bottom)

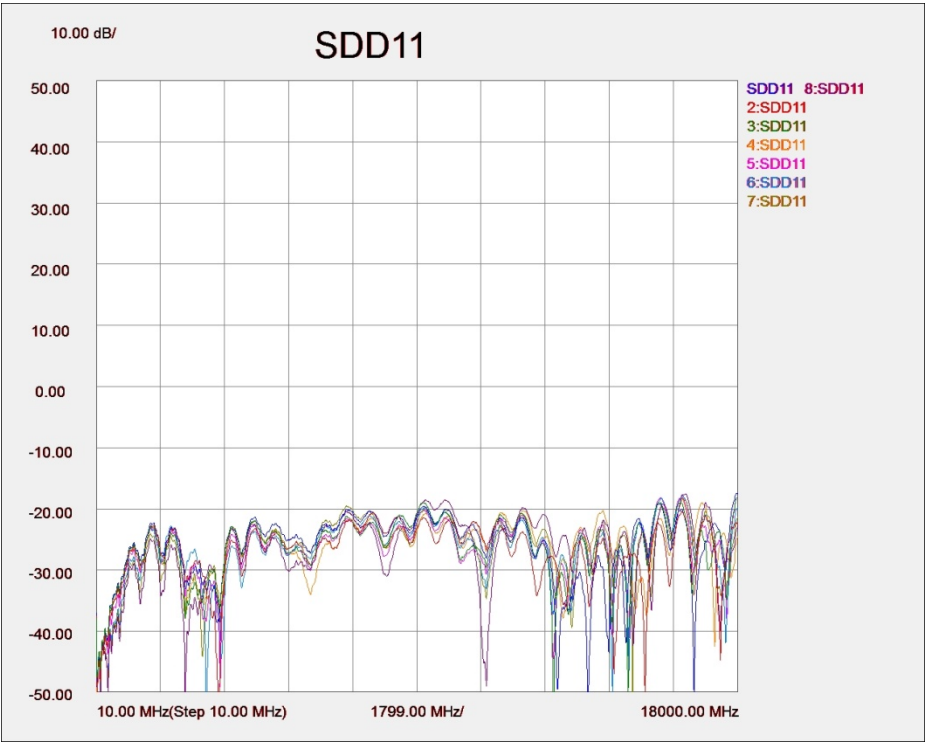


Figure 11. Typical TPA balanced return loss, differential termination replacing connector

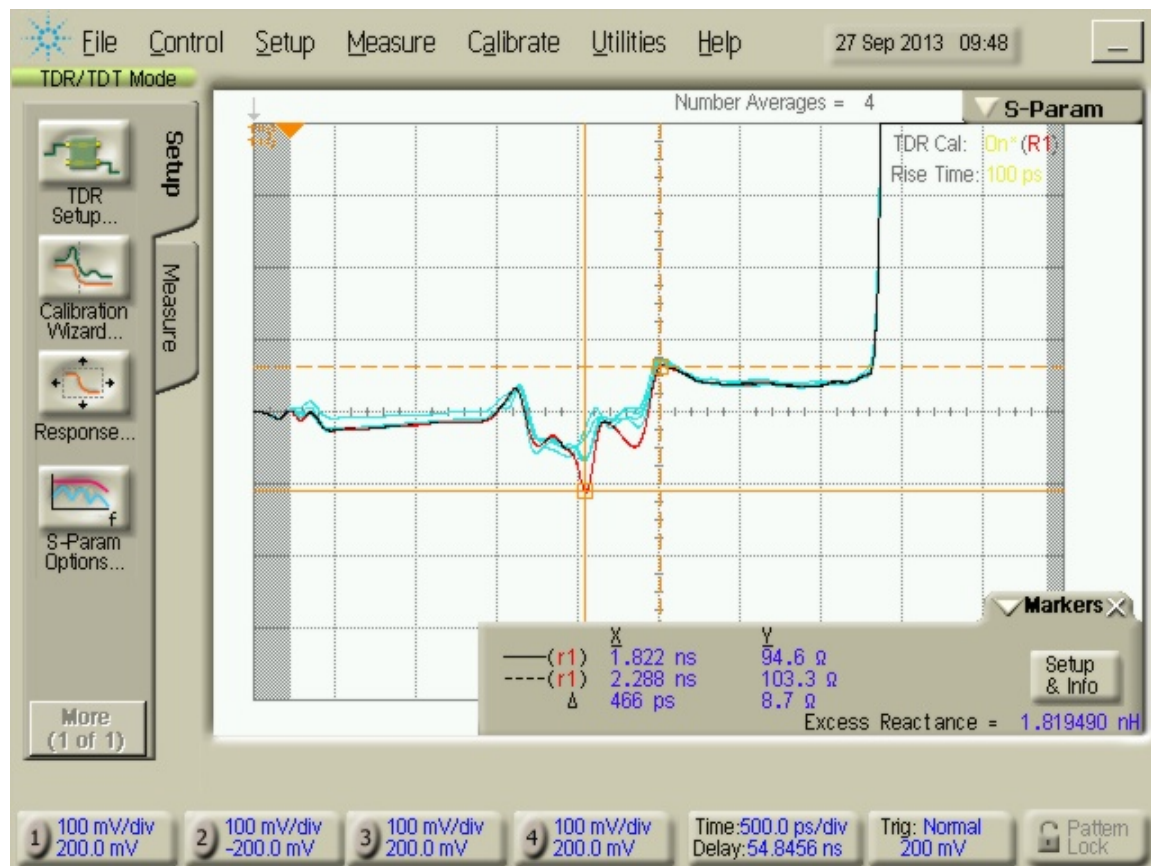


Figure 12. Typical differential TDR of TPA-P connected to TPA-R at 100 ps Rise Time, example differential pairs. (Equivalent to 70ps, 20% to 80%.)

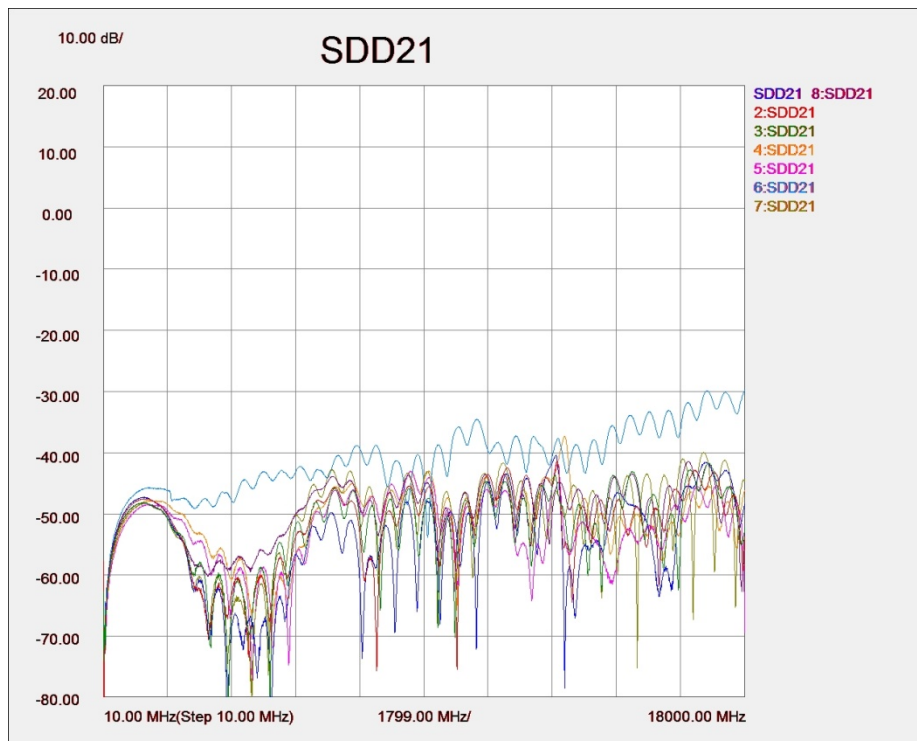
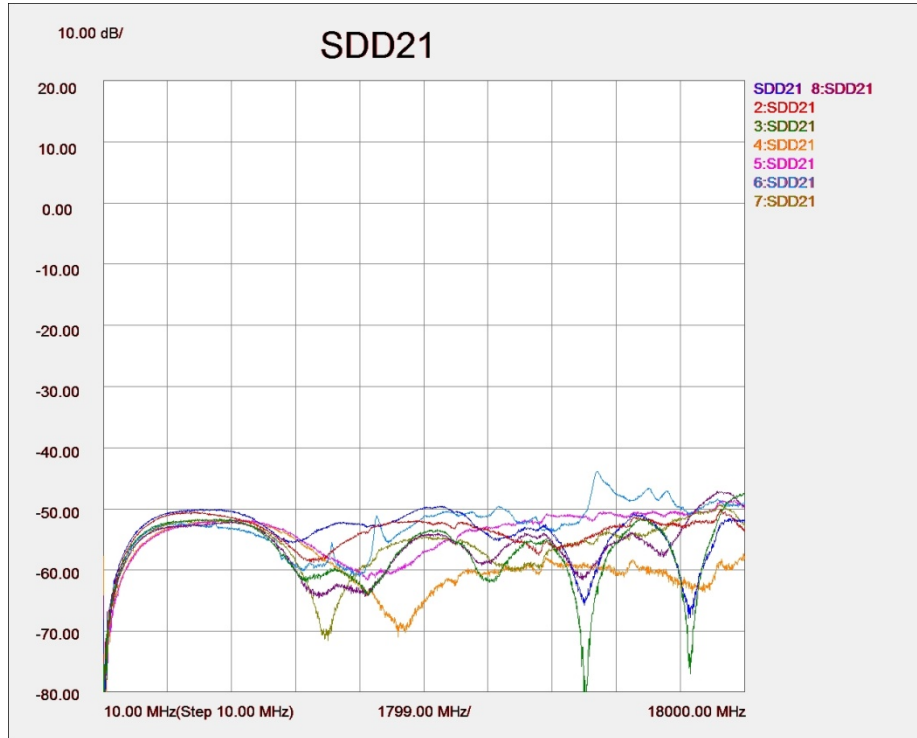


Figure 13. Typical Differential NEXT, without connector (top) and with mated connectors (bottom), adjacent differential pairs, all differential pairs terminated at both ends

## PCIe M.2 TPA Socket 3 Accessories and Optional Accessories

### PCIeM2-TPA3-R Accessories

The PCIeM2-TPA3-R (Device TPA) is supplied with Device Support Adapter materials that allow the user to properly retain and stabilize a device under test. (Ref. Wilder Technologies kit part number 600-1097-000.)

The figure, below, illustrates the installation and a typical application of the Device Support Adapter materials.

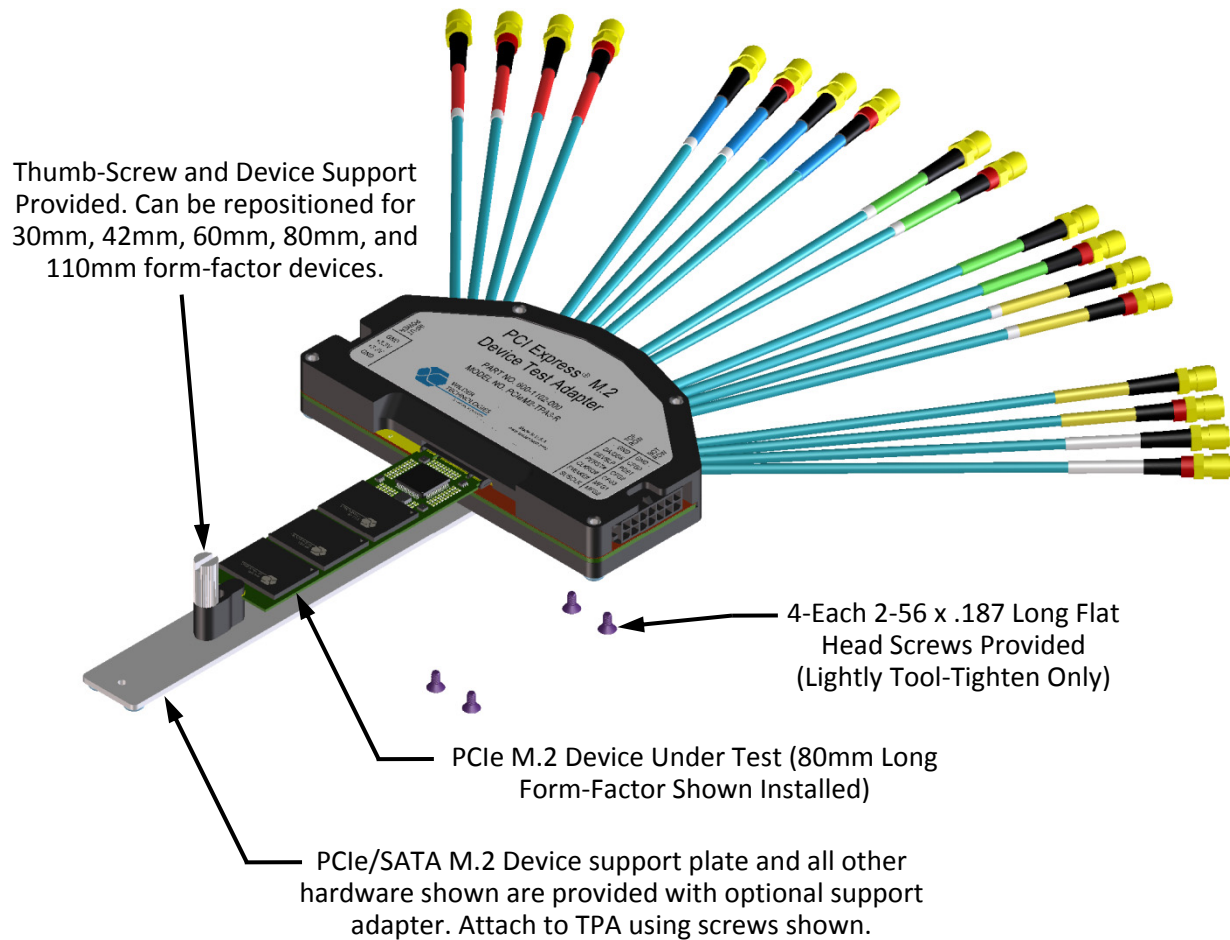


Figure 14. PCIeM2-TPA3-R Device Support Adapter

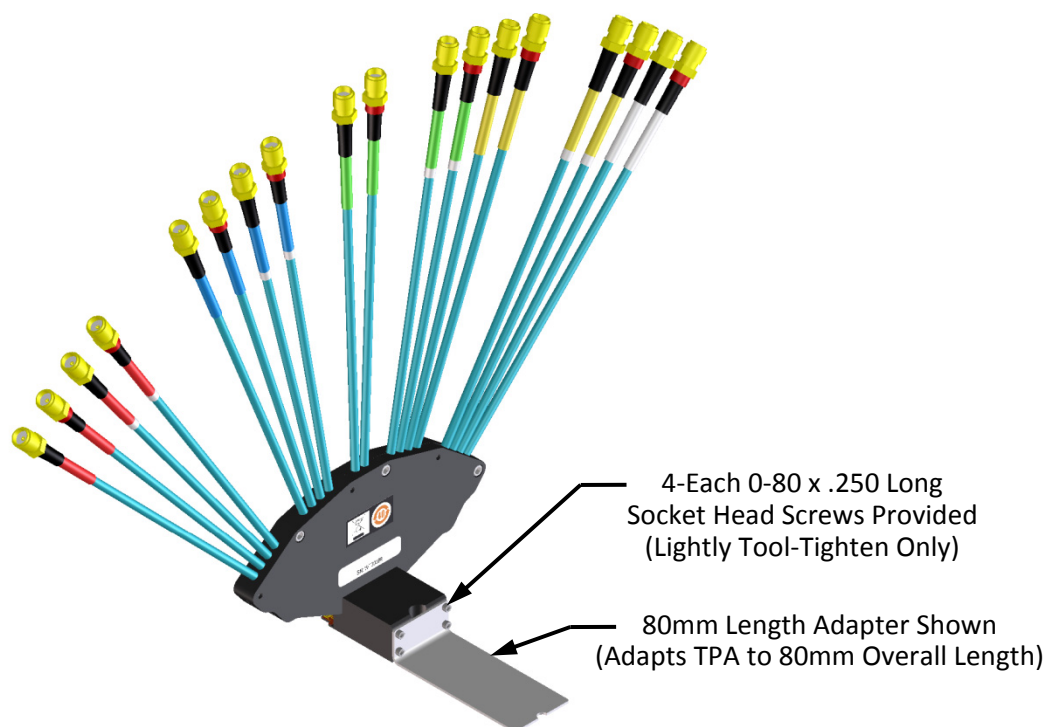
## PCIeM2-TPA3-P Optional Accessories

The PCIeM2-TPA3-P (Host TPA) can be optionally supplied with Host TPA Length Adapter materials. These materials allow the user to properly retain and stabilize the PCIe M.2 Host TPA when installed in host systems that are designed to accept devices with overall lengths of 42mm, 60mm, 80mm, or 110mm. The table below indicates the Wilder Technologies part numbers for an optional complete kit of length adapters and part numbers for individual specific length adapters. The PCIeM2-TPA3-P (Host TPA) is 30 mm long without the optional Length Adapters.

**Table 7. Optional PCIe/SATA M.2 Host TPA Length Adapters**

WILDER TECHNOLOGIES PART NUMBER	DESCRIPTION
600-1096-000	Complete Kit – Contains Adapters for 42mm, 60mm, 80mm, 110mm, and Hardware
600-1096-042	Contains Adapter for 42mm Applications and Mounting Hardware
600-1096-060	Contains Adapter for 60mm Applications and Mounting Hardware
600-1096-080	Contains Adapter for 80mm Applications and Mounting Hardware
600-1096-110	Contains Adapter for 110mm Applications and Mounting Hardware

The figure, below, illustrates the installation and typical application of the optional Host TPA Length Adapter materials.



**Figure 15. PCIeM2-TPA3-P Device Length Adapter**

## Wilder Technologies, LLC – Limited Warranty

Wilder Technologies, LLC warrants that each Test Adapter, 1) is free from defects in materials and workmanship and, 2) conforms to Wilder Technologies specifications for a period of 12 months. **See Consumable and Fragile Material Warranty for exceptions to the 12 month warranty**

The warranty period for a Test Adapter is a specified, fixed period commencing on the date of ship from Wilder Technologies, LLC. If you did not purchase your Test Adapter directly from Wilder Technologies, LLC, the serial number and a valid proof of purchase will be required to establish your purchase date. If you do not have a valid proof of purchase, the warranty period will be measured from the date of ship from Wilder Technologies, LLC.

If, during the warranty period, the Test Adapter is not in good working order, Wilder Technologies, LLC will, at its option, repair or replace it at no additional charge, except as is set forth below. In some cases, the replacement Test Adapter may not be new and may have been previously installed. Regardless of the Test Adapter's production status, Wilder Technologies, LLC appropriate warranty terms apply.

### **Consumable and Fragile Material Warranty**

Wilder Technologies, LLC warrants that consumable materials and all fragile materials supplied by Wilder Technologies, LLC either as part of an instrument or system, or supplied separately, will be free from defects in material and workmanship at the time of shipment.

### **Extent of Warranty**

The warranty does not cover the repair or exchange of a Test Adapter resulting from misuse, accident, modification, unsuitable physical or operating environment, improper maintenance by you, or failure caused by a product for which Wilder Technologies, LLC is not responsible. The warranty is voided by removal or alteration of Test Adapter or parts identification labels. The initial three months are unconditional; the remaining months excludes plugs, receptacles and SMA connectors. Connectors are wear items and excluded from the warranty after the initial three months.

**These warranties are your exclusive warranties and replace all other warranties or conditions, express or implied, including but not limited to, the implied warranties or conditions or merchantability and fitness for a particular purpose. These warranties give you specific legal rights and you may also have other rights which vary from jurisdiction to jurisdiction. Some jurisdictions do not allow the exclusion or limitation of express or implied warranties, so the above exclusion or limitation may not apply to you. In that event, such warranties are limited in duration to the warranty period. No warranties apply after that period.**

### **Items Not Covered by Warranty**

Wilder Technologies, LLC does not warrant uninterrupted or error-free operation of a Test Adapter.

Any technical or other support provided for a Test Adapter under warranty, such as assistance via telephone with "how-to" questions and those regarding Test Adapter set-up and installation, will be provided **WITHOUT WARRANTIES OF ANY KIND**.

### **Warranty Service**

Warranty service may be obtained from Wilder Technologies, LLC by returning a Wilder Technologies, LLC Returns Material Authorization and the Test Adapter to Wilder Technologies, LLC during the warranty period. To obtain RMA number, contact [support@wilder-tech.com](mailto:support@wilder-tech.com).

You may be required to present proof of purchase or other similar proof of warranty entitlement. You are responsible for any associated transportation charges, duties and insurance between you and Wilder Technologies, LLC. In all instances, you must ship Test Adapters in Wilder Technologies, LLC approved packaging. Information on packaging guidelines can be found at: [www.wilder-tech.com](http://www.wilder-tech.com). Wilder Technologies, LLC will ship repaired or replacement Test Adapter Delivery Duty Prepaid (DDP) and will pay for return shipment. You will receive title to the repaired or replacement Test Adapter and you will be the importer of record.

## Wilder Technologies, LLC – Terms & Conditions of Sale

1. **Other Documents:** This Agreement may NOT be altered, supplemented, or amended by the use of any other document(s) unless otherwise agreed to in a written agreement signed by both you and Wilder Technologies, LLC. If you do not receive an invoice or acknowledgement in the mail, via e-mail, or with your Product, information about your purchase may be obtained at [support@wilder-tech.com](mailto:support@wilder-tech.com) or by contacting your sales representative.
2. **Payment Terms, Orders, Quotes, Interest:** Terms of payment are within Wilder Technologies, LLC's sole discretion, and unless otherwise agreed to by Wilder Technologies, LLC, payment must be received by Wilder Technologies, LLC prior to Wilder Technologies, LLC's acceptance of an order. Payment for the products will be made by credit card, wire transfer, or some other prearranged payment method unless credit terms have been agreed to by Wilder Technologies, LLC. Invoices are due and payable within the time period noted on your invoice, measured from the date of the invoice. Wilder Technologies, LLC may invoice parts of an order separately. Your order is subject to cancellation by Wilder Technologies, LLC, in Wilder Technologies, LLC's sole discretion. Unless you and Wilder Technologies, LLC have agreed to a different discount, Wilder Technologies, LLC's standard pricing policy for Wilder Technologies, LLC-branded systems, which includes hardware, software and services in one discounted price, allocates the discount off list price applicable to the service portion of the system to be equal to the overall calculated percentage discount off list price on the entire system. Wilder Technologies, LLC is not responsible for pricing, typographical, or other errors in any offer by Wilder Technologies, LLC and reserves the right to cancel any orders resulting from such errors.
3. **Shipping Charges; Taxes; Title; Risk of Loss:** Shipping, handling, duties and tariffs are additional unless otherwise expressly indicated at the time of sale. Title to products passes from Wilder Technologies, LLC to Customer on shipment from Wilder Technologies, LLC's facility. Loss or damage that occurs during shipping by a carrier selected by Wilder Technologies, LLC is Wilder Technologies, LLC's responsibility. Loss or damage that occurs during shipping by a carrier selected by you is your responsibility. You must notify Wilder Technologies, LLC within 7 days of the date of your invoice or acknowledgement if you believe any part of your purchase is missing, wrong or damaged. Unless you provide Wilder Technologies, LLC with a valid and correct tax exemption certificate applicable to your purchase of Product and the Product ship-to location, you are responsible for sales and other taxes associated with the order. **Shipping dates are estimates only.**
4. **WARRANTY:** WILDER TECHNOLOGIES, LLC, warrants that the item(s) manufactured under the Buyer's contract shall be free from defects in materials and workmanship furnished by WILDER TECHNOLOGIES, LLC, and shall conform to the applicable drawings and specifications. WILDER TECHNOLOGIES, LLC'S liability herein, for breach of warranty, contract or negligence in manufacturing, shall be limited to repair or replacement. Repair or replacement of defective items will be applicable only if the Buyer notifies WILDER TECHNOLOGIES, LLC, by written notice within 30-days of delivery. All claims shall be addressed to: [support@wilder-tech.com](mailto:support@wilder-tech.com) or WILDER TECHNOLOGIES, LLC, 6101A East 18th Street, Vancouver, Washington 98661 U.S.A.; ATTENTION: Customer Service Manager. WILDER TECHNOLOGIES, LLC, reserves the right to inspect at the Buyer's plant all items claimed to be defective or nonconforming prior to authorizing their return. WILDER TECHNOLOGIES, LLC, assumes no liability for the results of the use of its components in conjunction with other electric, electronic or mechanical components, circuits and/or systems. The foregoing constitutes the sole and exclusive remedy of the Buyer and the exclusive liability of WILDER TECHNOLOGIES, LLC, and is IN LIEU OF ANY AND ALL OTHER WARRANTIES, STATUTORY, IMPLIED OR EXPRESSED AS TO MERCHANTABILITY, FITNESS FOR THE PURPOSE SOLD, DESCRIPTION, QUALITY, and PRODUCTIVENESS OR ANY OTHER MATTER. Without limiting the foregoing, in no event shall WILDER TECHNOLOGIES, LLC, be liable for loss of use, profit or other collateral, or for special and/or consequential damages.
5. **RETURNED GOODS:** WILDER TECHNOLOGIES, LLC, will accept only those goods for return that have been authorized for return. All goods authorized for return shall be assigned a Returned Material Authorization (RMA) Number. The RMA Number shall be clearly marked on the shipping container(s) and all documentation accompanying the goods authorized for return. The RMA Number shall be assigned by WILDER TECHNOLOGIES, LLC pursuant to the conditions set forth in Paragraph 4, WARRANTY.
6. **UNITED STATES GOVERNMENT CONTRACTS:** In the event this offer is accepted under Government contract, WILDER TECHNOLOGIES, LLC, agrees to accept clauses required by Government regulations and to waive WILDER TECHNOLOGIES, LLC conditions inconsistent therewith. WILDER TECHNOLOGIES, LLC, certifies that it is a regular manufacturer or dealer of the goods and/or services offered herein and that the prices offered do not exceed those charged to any customer for like quantities, services or materials under the same conditions.

## Compliance with Environmental Legislation

Wilder Technologies, LLC, is dedicated to complying with the requirements of all applicable environmental legislation and regulations, including appropriate recycling and/or disposal of our products.



### WEEE Compliance Statement

The European Union adopted Directive 2002/96/EC on Waste Electrical and Electronic Equipment (WEEE), with requirements that went into effect August 13, 2005. WEEE is intended to reduce the disposal of waste from electrical and electronic equipment by establishing guidelines for prevention, reuse, recycling and recovery.

Wilder Technologies has practices and processes in place to conform to the requirements in this important Directive.

In support of our environmental goals, effective January 1<sup>st</sup>, 2009 Wilder Technologies, LLC has partnered with EG Metals Inc. – Metal and Electronics Recycling of Hillsboro, Oregon, [www.egmetalrecycling.com](http://www.egmetalrecycling.com), to recycle our obsolete and electronic waste in accordance with the European Union Directive 2002/96/EC on waste electrical and electronic equipment ("WEEE Directive").

As a service to our customers, Wilder Technologies is also available for managing the proper recycling and/or disposal of all Wilder Technologies products that have reached the end of their useful life. For further information and return instructions, contact [support@wilder-tech.com](mailto:support@wilder-tech.com).

## Glossary of Terms

TERMINOLOGY	DEFINITION
Aggressor	A signal imposed on a system (i.e., cable assembly) to measure response on other signal carriers.
Decibel (dB)	Ten times the common logarithm (i.e. log10) of the ratio of relative powers.
Informative	The designation of a test that is not required for compliance but is considered important from a characterization standpoint. It is provided for informational purposes only.
Insertion loss	The ratio, expressed in dB, of incident power to delivered power.
PCIe	Peripheral Component Interconnect Express
PCIeM2-TPA3	PCIe M.2 Socket 3 Test Point Access. A specialized assembly that interfaces to an PCIe M.2 socket 3 receptacle or plug and enables access of signals for measurement or stimulation.
M.2	Formally called Next Generation Form Factor (NGFF)
Near-end crosstalk	Crosstalk that is propagated in a disturbed channel in the opposite direction as the propagation of a signal in the aggressor channel. The terminals of the aggressor channel and the victim channel are usually close to each other.
Normative	The designation of a test that is required for compliance.
Return Loss	The ratio, expressed in dB, of incident power to reflected power.
RoHS	Restriction of Hazardous Substances Directive
Serial ATA (SATA)	Serial Advanced Technology Attachment or Serial ATA. The protocol defined by SATA (see ATA8-AAM)
Serial Attached SCSI (SAS)	The set of protocols defined in SPL and the interconnect defined by the SAS-2.1 standard.
USB	Universal Serial Bus
Victim	A signal carrier on a system that has a response imposed on it by other signals in the system.

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